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IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applicants: ARAI, et al.

Filed: March 1, 2002

For: FABRICATION METHOD OF SEMICONDUCTOR INTEGRATED
CIRCUIT DEVICE



CLAIM FOR PRIORITY

Assistant Commissioner for Patents
Washington, D.C. 20231

March 1, 2002

Sir:

Under the provisions of 35 USC §119 AND 37 CFR § 1.55, Applicants hereby
claim the right of priority based on Patent Application No. 2001-118413 filed in Japan on
April 17, 2002.

Respectfully submitted,

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